



Fig.1

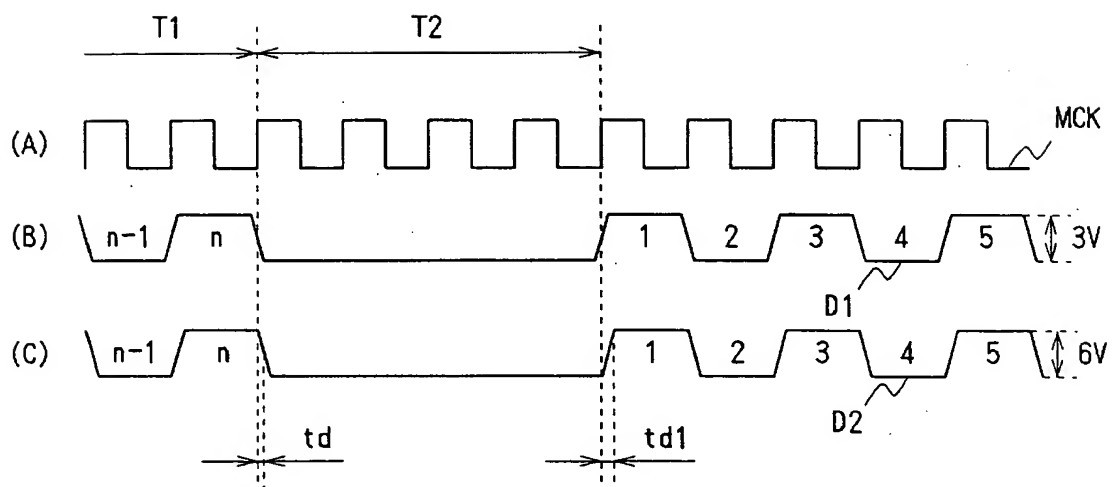


Fig.2

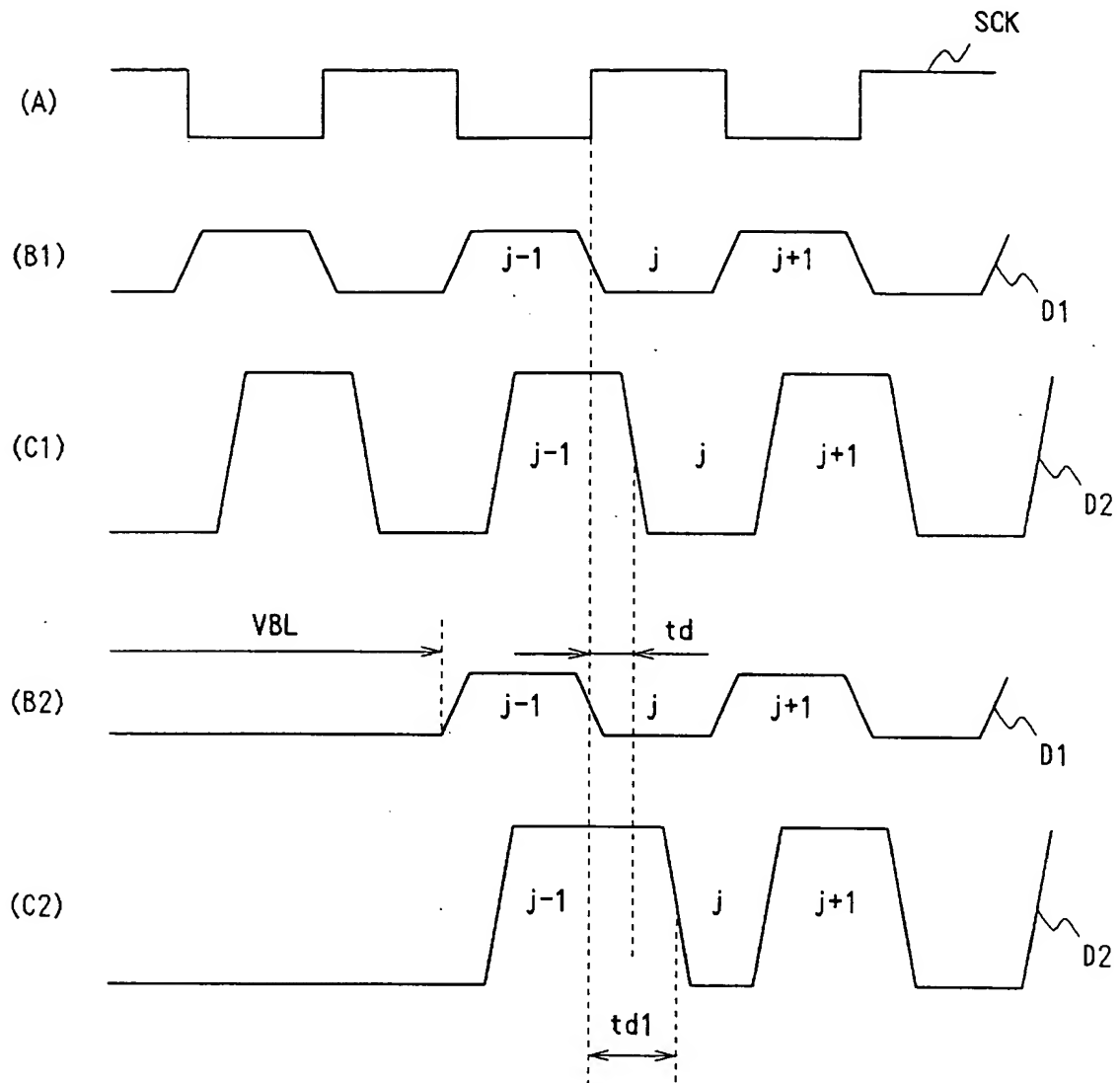


Fig.3

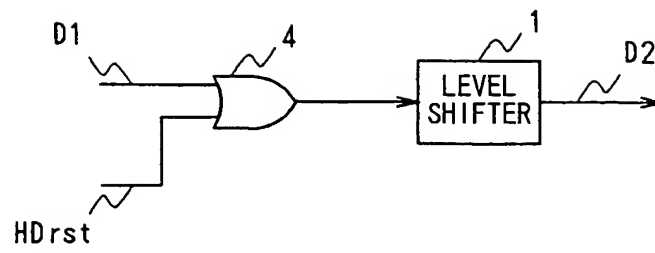


Fig.4

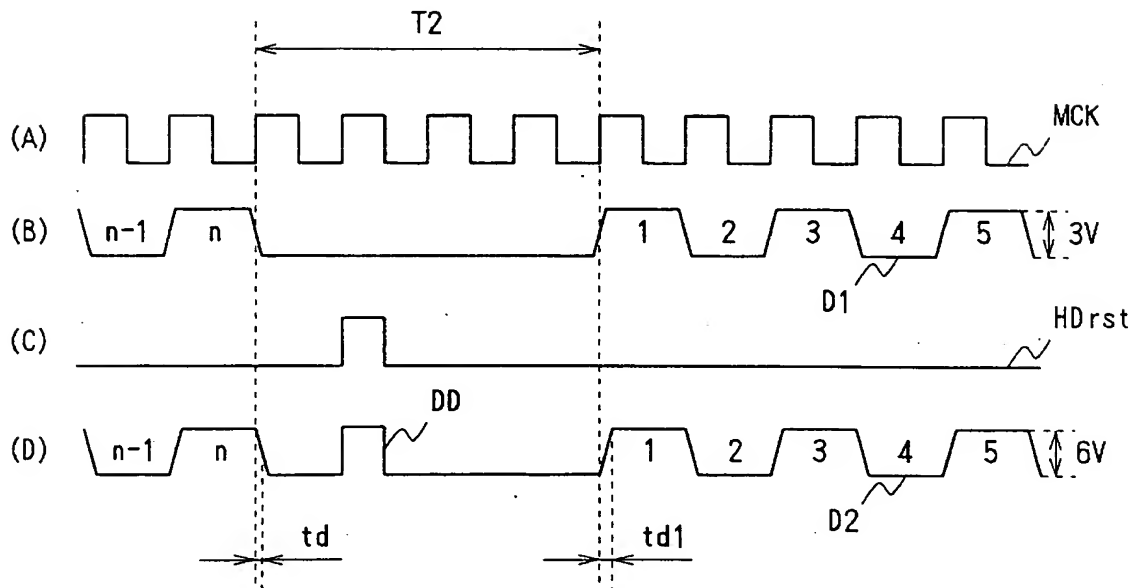


Fig.5

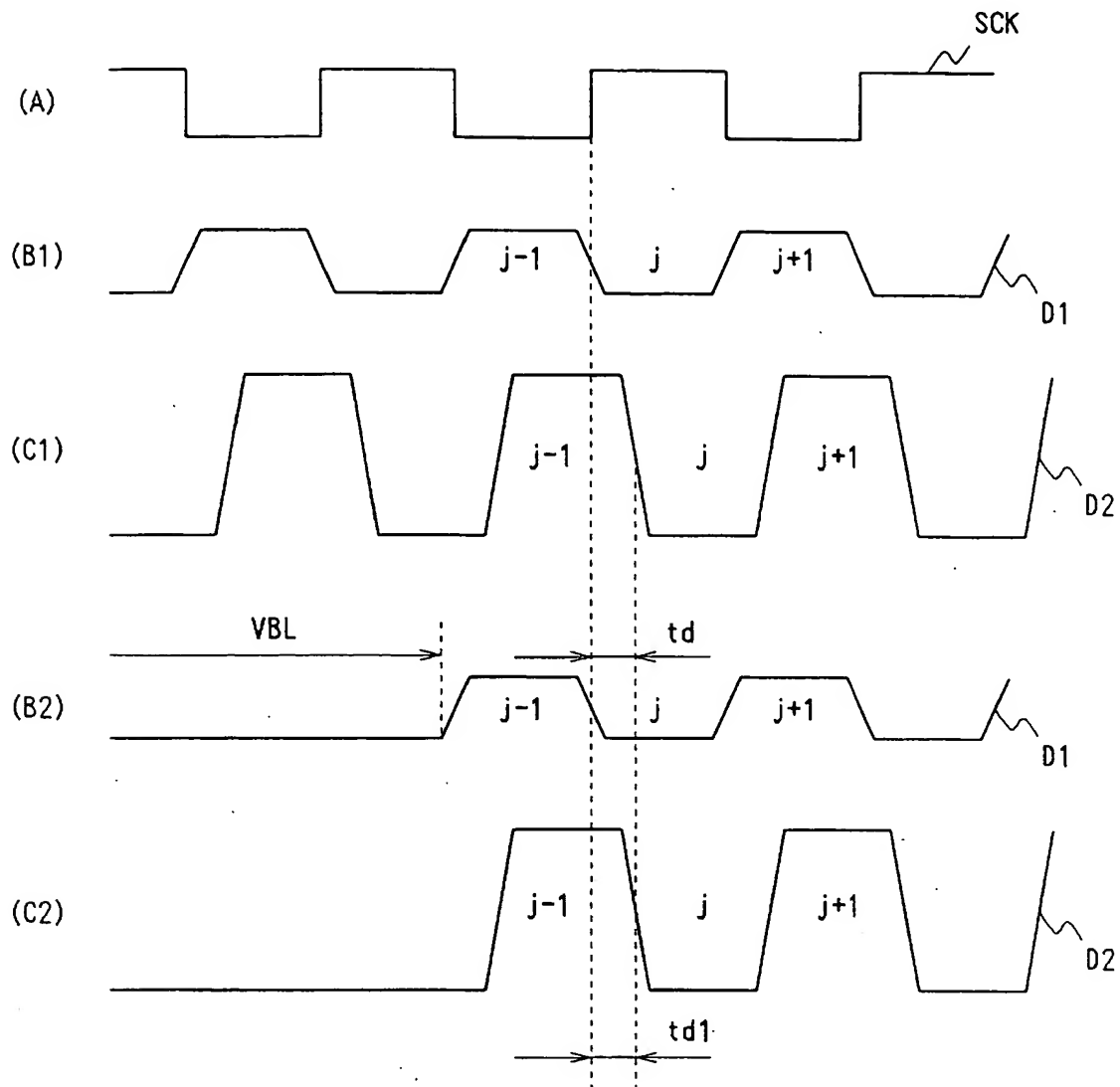


Fig.6

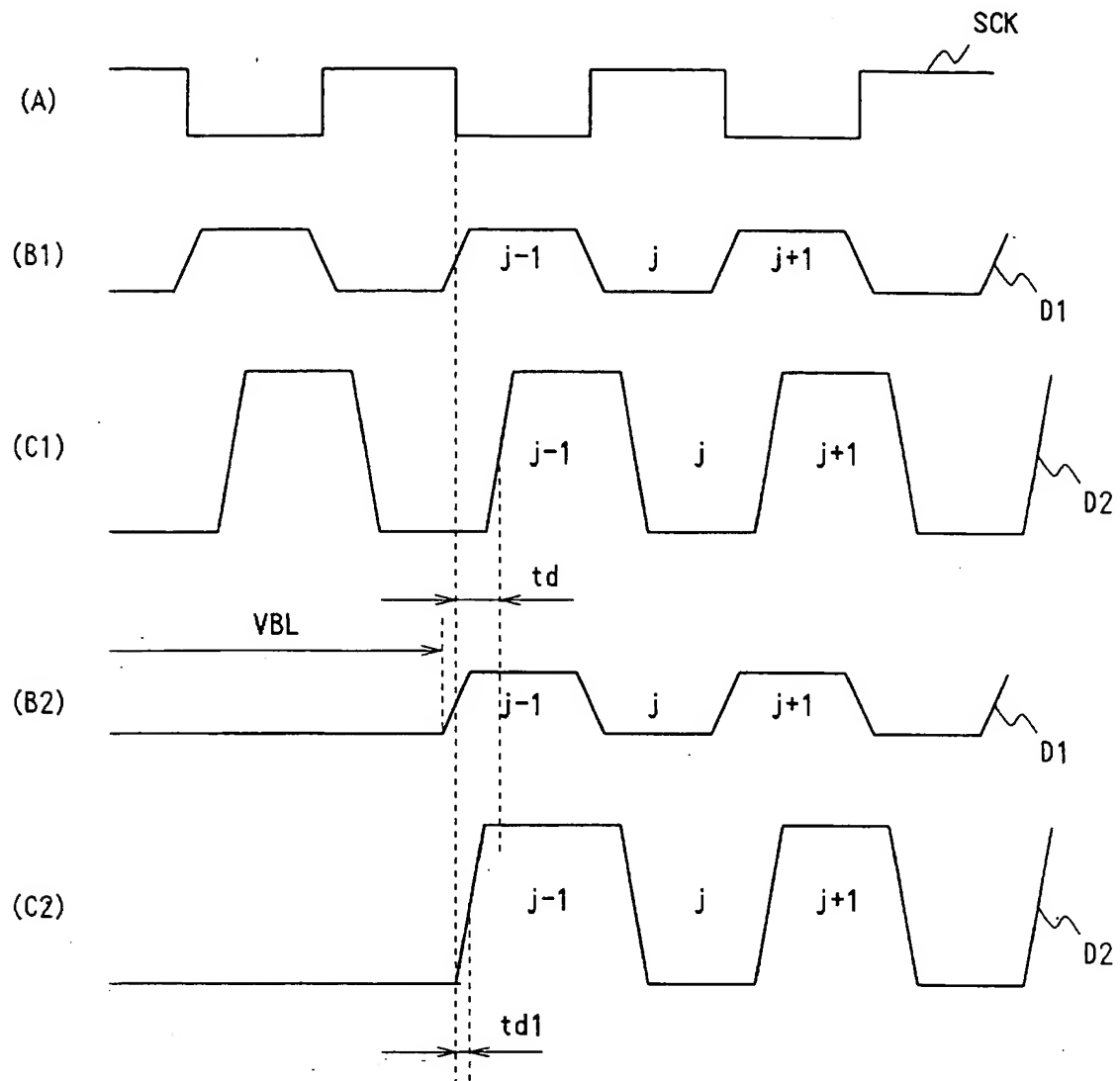
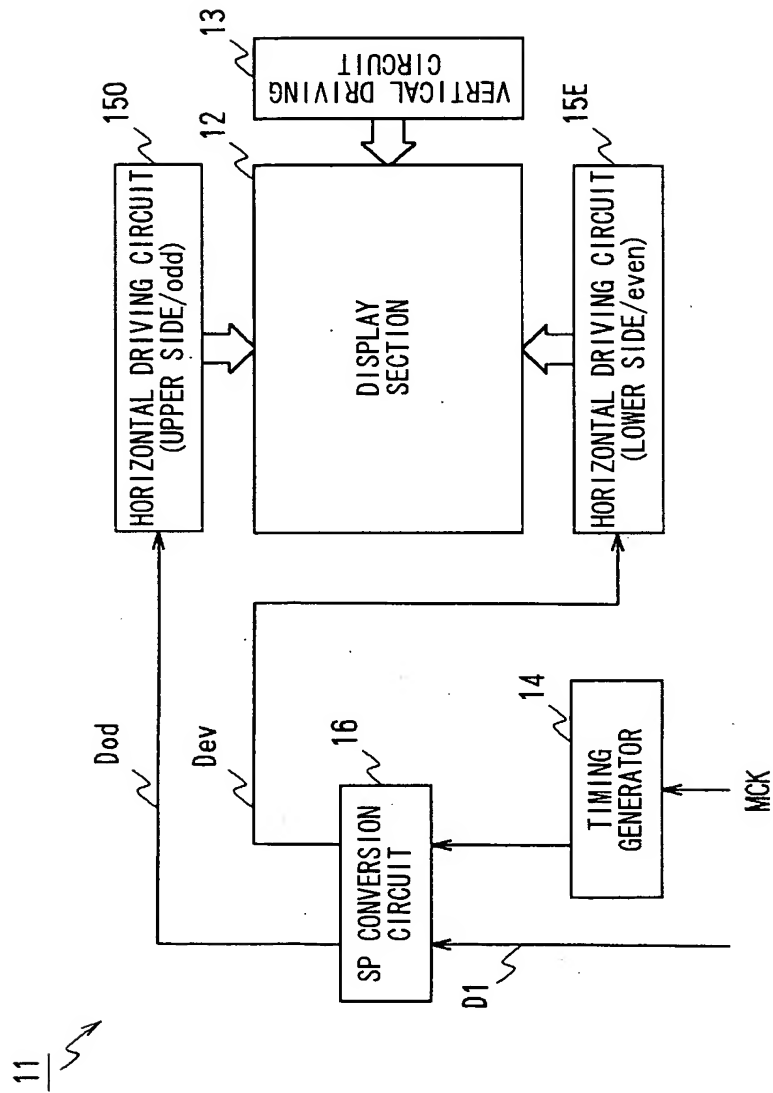


Fig.7



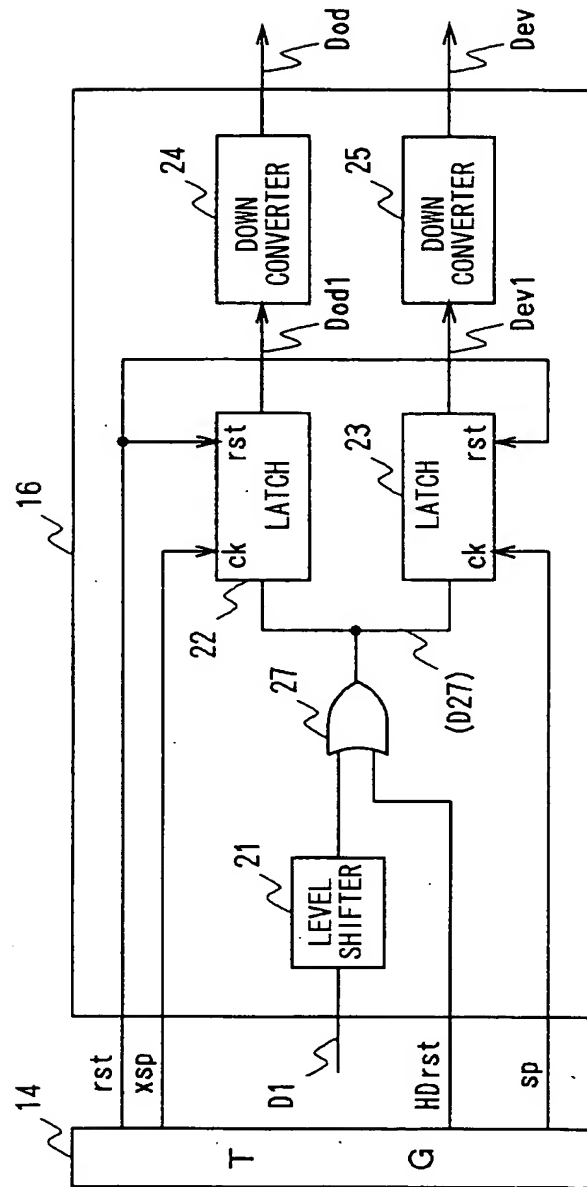


Fig.9

22, (23)

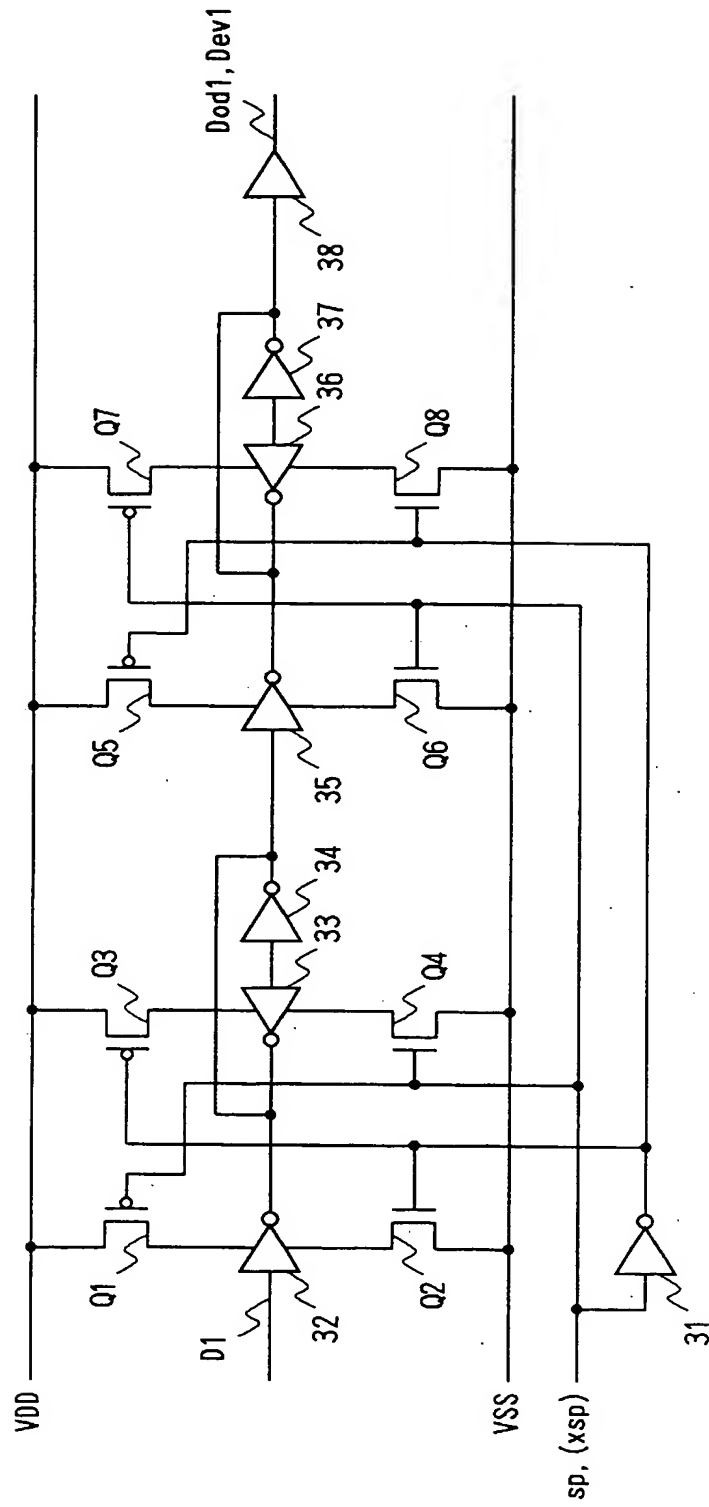


Fig.10



24, (25)

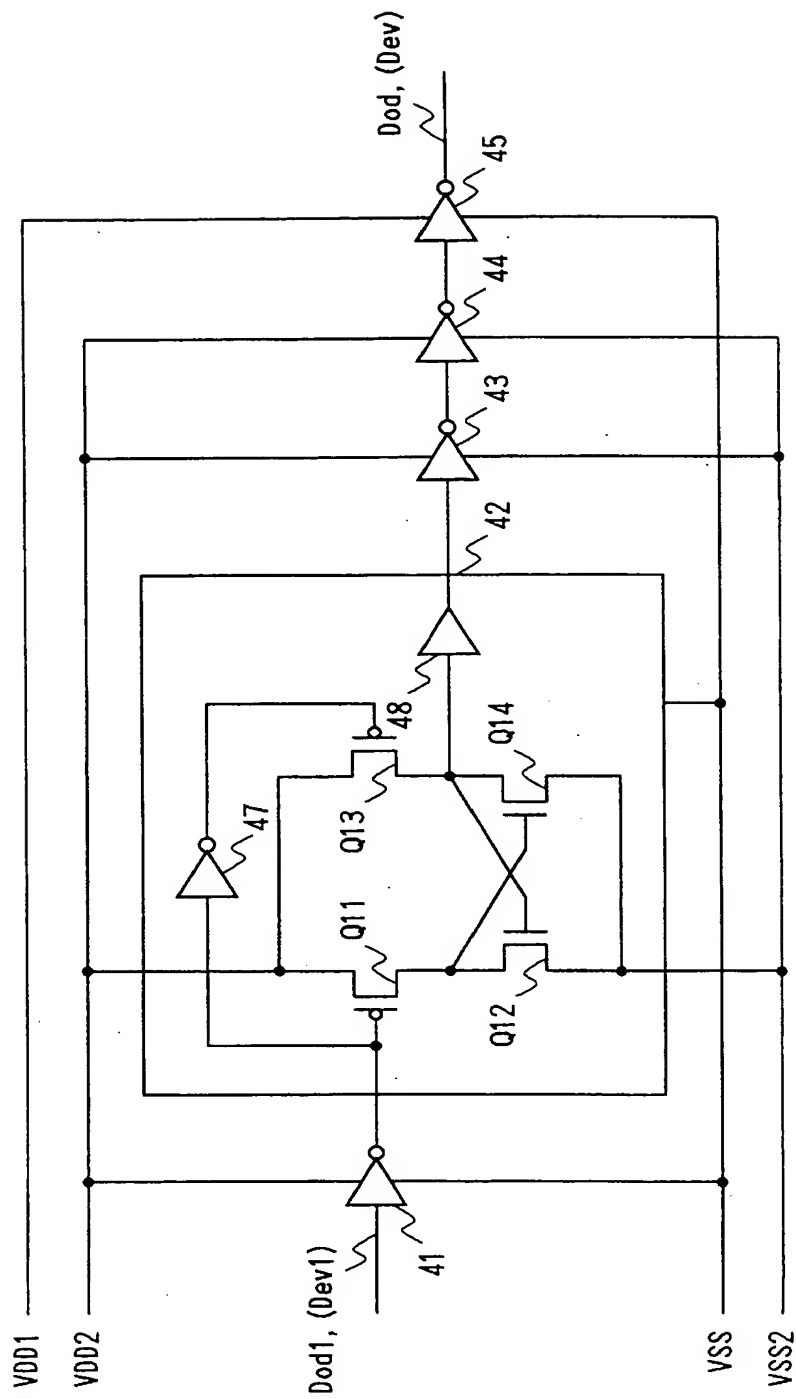


Fig.11

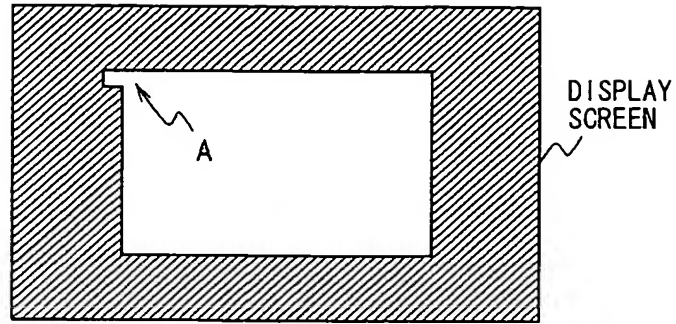


Fig.12

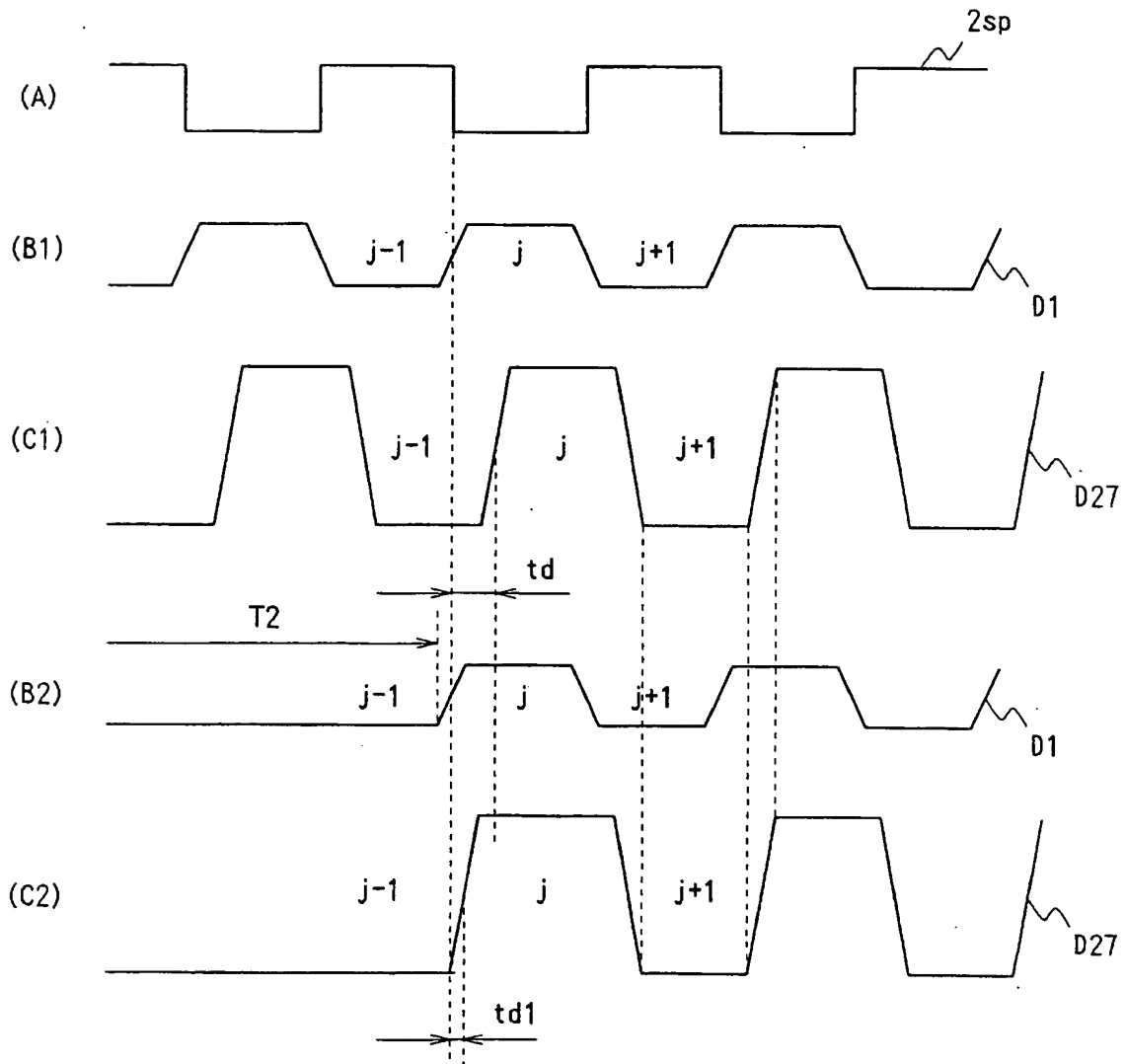


Fig.13

## Description of Reference Numerals

1, 21, 42 ..... level shifter, 4, 27..... OR circuit, 11  
..... liquid crystal display device, 12 ..... display  
section, 13 ..... vertical driving circuit, 14 ..... timing  
generator, 15O, 15E ..... horizontal driving circuit,  
16 ..... serial-to-parallel conversion circuit, 22, 23  
..... latch circuit, 24, 25 ..... down converter, 31 to  
37, 41, 43 to 47 ..... inverter, 38,48 ..... buffer, Q1  
to Q14 ..... transistor